

Electronic Acknowledgement Receipt

EFS ID:	1227907
Application Number:	10604491
Confirmation Number:	1490
Title of Invention:	Multiprocessor subsystem in SoC with bridge between processor clusters interconnection and SoC system bus
First Named Inventor:	Christos J. Georgiou
Customer Number:	23389
Filer:	Steven Fischman/Grace Colucci
Filer Authorized By:	Steven Fischman
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Request for Continued Examination (RCE)	FIS920030113US1_RCE.pdf	112787	no	2
Warnings:					
Information:					
2	Information Disclosure Statement (IDS) Filed	FIS920030113US1_IDS.pdf	3325380	no	5
Warnings:					
Information:					
3	NPL Documents	Almasi.pdf	639895	no	17
Warnings:					
Information:					
4	NPL Documents	Georgiou.pdf	512967	no	9
Warnings:					
Information:					
5	NPL Documents	IBMIIntroducesPowerPC.pdf	65857	no	2
Warnings:					
Information:					
6	NPL Documents	CoreConnectBusArchitecture.pdf	76881	no	2
Warnings:					
Information:					
7	NPL Documents	ARMProcessorCoresOverview.pdf	114203	no	2
Warnings:					
Information:					
8	NPL Documents	BrocadeMultiprotocolRoutingServices.pdf	1323412	no	14
Warnings:					
Information:					
9	Fee Worksheet (PTO-875)	fee-info.pdf	8207	no	2
Warnings:					

Information:	Total Files Size (in bytes):	6179589
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.		
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